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Using an in-**kernel emulator** for TPS development and testing. Hicks, F. Southwest Res. Inst., Warner Robins, GA, USA;. This paper appears in: AUTOTESTCON ...

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tech-kern: FreeBSD 5/6/7 **kernel emulator for NetBSD 2.x**

Hi, I have made an effort on making a FreeBSD 5/6/7 **kernel emulator** for NetBSD 2.x.

Many device drivers are shared between these two operating systems, ...

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circle.ch :: Furlongs Linux **Kernel Emulator**

Wez Furlong [1] started reading through some documents at MSDN and finally ended up with a small Linux **kernel emulator** for Windows. Amazing story ;-)

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FreeBSD 5/6/7 **kernel emulator for NetBSD 2.x**

Here is a case in which such a **kernel emulator** can be used: > > I think it is a good idea that FreeBSD and NetBSD shares as many USB drivers > as possible. ...

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0003181676 20070101.

Title

Compiling 65816 Forth with a 6502 metacompiler.

Conference information

1987 Rochester Forth Conference on Comparative Computer Architectures, Rochester, NY, USA, 1987.

Sponsor(s): Johns Hopkins Univ; Oak Ridge Nat. Lab; IEEE; et al.

Source

Journal of Forth Application and Research, {J-Forth-Appl-Res-USA}, 1987, vol. 5, no. 1, p. 236, 0 refs, CODEN: JFAREL, ISSN: 0738-2022, USA.

Author(s)

Anway-A.

Author affiliation

Anway, A., Acad. Comput., UW-Superior, Superior, WI.

Abstract

Summary form only given, substantially as follows. Having a new Apple IIGS and an old Forth metacompiler gave incentive to write Forth in the 65816 **native** code, a true 16-bit machine. The simple accumulator (A), memory (M), X and Y registers are preserved from the old 6502 into the new 65816 but they can be independently set to 16 bits or 8 bits; or exact 6502 **emulation**. The conversion stages were: (1) Write a new Forth assembler, a converted 6502, into a multiple mode assembler. The programmer tells the assembler the mode with words 6502- 816andand 816and- 816and 816--, the normal NEXT exit 816and- being 16 bits M&A, 8 bits X&Y. (2) Using 6502- code Forth on the IIGS, write test examples of code in 816and- **native** code. Words were written as CODE (DO)' CLC, XCE, 20 .# REP, . . . SEC, XCE, NEXT JMP, END-CODE to redefine code into 816and-, run, and put back into 6502- else crash. (3) Rewrite the old meta to a new meta, writing I/O references in old 6502- mode. (4) Compiles the new 816and- **meta-kernel** relying on the metacompiler forcing all interpretation to take place from the old dictionary. Only when the new **kernel** starts up alone does the machine transfer to the true 65816 **native** mode.

Descriptors

☒ APPLE-COMPUTERS; ☒ FORTH; ☒ MICROCOMPUTER-APPLICATIONS; ☒ PROGRAM-COMPILERS.

Classification codes

C6150C Compilers-interpreters-and-other-processors*.

Keywords65816-Forth; 6502-metacompiler; Apple-IIIS; **native-code**; 16-bit-machine; Forth-assembler; multiple-mode-assembler; I/O-references; **meta-kernel**; interpretation; dictionary; 16-bit.**Treatment codes**

P Practical.

Numerical indexing

word length: 1.6E01 bit.

Language

English.

Publication type

Conference-paper; Journal-paper.

Availability

CCCC: 0738-2022/87/\$2.75.

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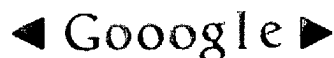
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